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Date Mailed : March 5, 2012
Serial No.   : 09/385394
Patent No.   : 8,074,055
Patent Issued :December 6, 2011
Inventor(s)   :John S. Yates, Jr., et al.
Title   :ALTERING DATA STORAGE CONVENTIONS OF A
              PROCESSOR WHEN EXECUTION FLOWS FROM FIRST
              ARCHITECTURE CODE

Request for Certificate of Correction

Consideration has been given your request for the issuance of a certificate of correction for the above-identified patent.

Respecting the alleged error(s) noted in your request: The proposed correction at column 107, lines 51 are hereby denied because there is an ambiguity as to exactly where insert (line 51) should occur within the claim. The ambiguity arises because the typeset line number in the patent sits half way between two lines, and because the lines of the claim do not mesh properly with the typeset line number.

In view of the foregoing your request in this matter is hereby denied.

A certificate of correction will issue for the remaining errors, noted in your request.

Magdalene Talley
For Mary Diggs, Supervisor
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